

**SESSION 1    OPENING AND KEYNOTE ADDRESS**

09:00 – 09:30 Jaco van de Pol, Boudewijn Haverkort

*Welcome*

09:30 – 10:30 Luboš Brim

*Distributed Verification: Exploring the Power of Raw Computing Power*

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**10:30 - 11:00    COFFEE BREAK**

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**SESSION 2    EXPLICIT MODEL CHECKING**

11:00 - 11:30 Christophe Pajault, Jean-François Pradat-Peyre

*Distributed Colored Petri Net Model-Checking with Galaxy*

11:30 - 12:00 Jiří Barnat, Pavel Moravec

*Parallel Algorithms for Finding SCCs in Implicitly Given Graphs*

12:00 - 12:30 Anton Wijs (work-in-progress report)

*Distributed Guided State Space Exploration for Scheduling Problems*

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**12:30 - 14:30    LUNCH BREAK**

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**SESSION 3    SYMBOLIC MODEL CHECKING**

14:30 - 15:00 Jonathan Ezekiel, Gerald Luetzgen, Radu Siminiceanu

*Can Saturation be Parallelised? On the Parallelisation of a Symbolic State-Space Generator*

15:30 - 15:30 Erika Ábrahám, Bernd Becker, Martin Fränzle, Christian Herde, Tobias Schubert

*Parallel SAT-Solving in Bounded Model Checking*

15:30 - 16:00 Jiří Barnat, Luboš Brim, Ivana Černá, Milan Česka, Jana Tůmová (work-in-progress report)

*Distributed Qualitative LTL Model Checking of Markov Decision Processes*

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**16:00 - 16:30    COFFEE BREAK**

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**SESSION 4    DISCUSSION, STAND-UP RESEARCHERS AND CLOSING**

**SESSION 1    OPENING**

09:00 - 10:00    Andrea Corradini **invited talk**  
*Towards a categorical semantics of concurrency for adhesive rewriting systems*

10:00 - 10:30    Ana Paula Lüdtké Ferreira, Leila Ribeiro and Luciana Foss  
*Formal Verification of Object-Oriented Graph Grammars Specifications*

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10:30 - 11:00    COFFEE BREAK

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**SESSION 2    THEORETICAL ASPECTS**

11:00 - 11:30    Tihamer Levendovszky, Ulrike Prange and Hartmut Ehrig  
*Termination Criteria for DPO Transformations with Injective Matches*

11:30 - 12:00    Lars Birkedal, Troels Damgaard, Arne Glenstrup and Robin Milner  
*Matching of Bigraphs*

12:00 - 12:15    Tobias Heindel and Filippo Bonchi  
*Adhesive DPO Parallelism for monic matches*

12:15 - 12:30    Claudia Faggian and Mauro Piccolo  
*A graph abstract machine describing event structure composition*

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12:30 - 14:30    LUNCH BREAK

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**SESSION 3    QUANTITATIVE ASPECTS**

14:30 - 15:00    Piotr Kosiuczenko and Georgios Lajios  
*Simulation of Generalised Semi-Markov Processes based on Graph Transformation Systems*

15:00 - 15:30    László Gönczy and Dániel Varró  
*Modeling and Verification of Reliable Messaging by Graph Transformation Systems*

15:30 - 15:45    Vitali Kozioura  
*Verification of Random Graph Transformation Systems*

15:45 - 16:00    *Preparation for discussion session*

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16:00 - 16:30    COFFEE BREAK

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**SESSION 4    DISCUSSION SESSION**

16:30 - 18:00    *Ambitions for Graph Transformation-Based Verification*

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08:15 - 08:30 WELCOME

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**SESSION 1**

08:30 - 09:30 Thomas Brihaye (Univ. Mons-Hainaut, Belgium)

*Verification and control of o-minimal hybrid systems*

09:30 - 10:30 Franck Cassez (CNRS/IRCCyN, France)

*Monitoring and Fault-Diagnosis with Digital Clocks*

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10:30 - 11:00 COFFEE BREAK

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**SESSION 2**

11:00 - 12:00 Patricia Bouyer (LSV-CNRS/ENS-Cachan, France)

*Weighted Timed Games: Model-Checking and Games*

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12:30 - 14:30 LUNCH BREAK

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**SESSION 3**

14:00 - 15:00 Kim G. Larsen (Aalborg Univ. Denmark)

*TBA*

15:00 - 16:00 Jean-François Raskin (Univ. Libre de Bruxelles, Belgium)

*A lattice theory to solve games of imperfect information*

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16:00 - 16:30 COFFEE BREAK

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**SESSION 1 CHANNEL-BASED INTERACTION**

- 09:00 - 09:30 Gabriel Ciobanu, Cristian Prisacariu  
*Coordination by Timers for Channel-Based Anonymous Communications*
- 09:30 - 10:00 Sascha Klueppelholz, Christel Baier  
*Symbolic Model Checking for Channel-based Component Connectors*
- 10:00 - 10:30 Marco Antonio Barbosa, Luis Soarez Barbosa  
*Configurations of Web Service*

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10:30 - 11:00 COFFEE BREAK

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**SESSION 2 COORDINATION AND ADAPTION**

- 11:00 - 11:30 Matteo Casadei, Luca Gardelli, Mirko Viroli  
*Simulating Emergent Properties of Coordination in Maude: the Collective Sorting Case*
- 11:30 - 12:00 Antonio Brogi, Javier Camara, Carlos Canal, Javier Cubo, Ernesto Pimentel  
*Dynamic Contextual Adaptation*
- 12:00 - 12:30 Andrea Omicini  
*Formal ReSpecT in the A&A Perspective*

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12:30 - 14:30 LUNCH BREAK

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**SESSION 3 THEORY OF COMPONENT INTERACTION**

- 14:30 - 15:00 Michael Kohler and Heiko Rolke  
*Dynamic Transition Refinement*
- 15:00 - 15:30 J. K. F. Bowles, S. Moschoyiannis  
*Concurrent Logic and Automata Combined: A semantics for Components*
- 15:30 - 16:00 Tom Chothia, Jetty Kleijn  
*Q-Automata: Modelling the Resource Usage of Concurrent Components*

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16:00 - 16:30 COFFEE BREAK

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**SESSION 4 PANEL DISCUSSION**

16:30 - 18:00

**SESSION 1 WELCOME**

08:30 - 09:00 Werner Damm, CvO Universität Oldenburg  
Wolfgang Paul, Universität des Saarlandes  
Christine Petry, DFG  
Rainer Jansen, BMBF

**SESSION 2A SURVEY TALK**

09:00-09:30 Werner Damm, CvO Universität Oldenburg  
*The SFB/TR AVACS*

**SESSION 2B SELECTED PRESENTATIONS FROM AVACS**

09:30-10:00 Johannes Faber, CvO Universität Oldenburg  
*Verifying real-time aspects of the European Train Control System*  
10:00-10:30 Martin Fränzle, CvO Universität Oldenburg  
*A tight integration of SAT and interval constraint propagation*

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10:30 - 11:00 COFFEE BREAK

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**SESSION 2C SELECTED PRESENTATIONS FROM AVACS**

11:00-11:30 Sven Schewe, Universität des Saarlandes  
*Verifying Partial Designs*  
11:30-12:00 Jörg Bauer, Universität des Saarlandes  
*Platoon Modelling and Verification*

**SESSION 3A INVITED PRESENTATIONS**

12:00-12:45 Joost-Pieter Katoen, RWTH Aachen  
*Recent Advances in Probabilistic Model Checking*

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12:45 - 14:00 LUNCH BREAK

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**SESSION 3B INVITED PRESENTATIONS**

- 14:00-14:45 Gert Döhmen, Airbus D  
*Industrialisation of Formal Methods in System Development*
- 14:45-15:30 Warren A. Hunt, Jr., The University of Texas at Austin  
*Mathematics-Based IP*

**SESSION 4A SURVEY TALK**

- 15:30-16:00 Wolfgang Paul, Universität des Saarlandes  
*The BMBF Project Verisoft*

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16:00 - 16:30 COFFEE BREAK

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**SESSION 4B SELECTED PRESENTATIONS FROM VERISOFT**

- 16:30 - 17:00 Lars Kuhtz, Universität des Saarlandes  
*Validating C-to-Assembler Translations*
- 17:00 - 17:30 Tobias Nipkow, TU München  
*Improving ISABELLE in the Verisoft Context*
- 17:30 - 18:00 Wolfgang Büttner, OneSpin Solutions  
*Automation and Robustness of Correctness Proofs for Processors*
- 18:00 - 18:30 Steffen Knapp, Universität des Saarlandes  
*Status Report: The Formal Verification of a Distributed Realtime System*